

Figure 1 illustrates a cryptographic process. It starts with a key stream $Y Q E K E I$ and a message $R T O U G M O Y C P I$. These are combined via XOR to produce $V N M E C M Y P K G T Q Y C O A T T$. This intermediate result is then XORed with $C U W V C$ to yield $M Q P K G T Q Y C O A T T 2$. This is further XORed with $P J F T C P V$ to produce $F Y W M Q P K G T Q Y C O A T T 2$. Finally, XORing with $M N Q P C O$ results in the final output $M Q P K G T Q Y C O A T T 2$.

[illegible][illegible]

Ky gog<	I o lpcTqdc		
Cfegwufy <	Urdlo fgi [eg To fFqcl.T]ep'36 Ogi [pTo fFqclqcl'qdi T]ep'37 S88.S87B.S6 : S87B.S87B : S87B.S87B Ogi [pTo fFqclqcl'qdi T]ep'37 [pT] . S4.'54343543E.		
Qdgm:	Dwf q'clgkly'q'qki y gl		
P el y c'urteay epk<	Rqlmfwufy ap'		
P el y cl'apmpe	Ugi go vgl' y o'nc ay [ej]		
Fagmwp'	o i'tp flcpTlqayuy u' CRD467.RY UB4		
Urty f d f el e'	o i'tp flcmndT 6wy uy u' CRD444.RQUB4		
Urten<	E		
Fec'atcc'	UW ej g'23: 20		T[u]p'34

Figure 1: Schematic diagram of the proposed power system. The diagram shows a power system with a central busbar connected to three main components: a generator (GEN), a transformer (T), and a motor (M). The generator is labeled 'GEN: 1000 MVA, 110 kV, 0.05 pu reactance'. The transformer is labeled 'T: 1000 MVA, 110/220 kV, 0.05 pu reactance'. The motor is labeled 'M: 1000 MVA, 110 kV, 0.05 pu reactance'. The busbar is connected to a load (L) labeled 'L: 1000 MVA, 110 kV, 0.05 pu reactance'. The system is grounded through a reactor (R) labeled 'R: 1000 MVA, 110 kV, 0.05 pu reactance'. The diagram also shows a fault (F) on the line between the generator and the busbar, and a fault (F) on the line between the busbar and the motor. The fault is labeled 'F: 1000 MVA, 110 kV, 0.05 pu reactance'.

Figure 1 is a logic diagram of a 16-bit carry-lookahead adder. The diagram shows a hierarchical structure of logic gates. At the top, two 16-bit inputs, A and B, are fed into a network of AND gates. The outputs of these AND gates are connected to a series of OR gates, which are in turn connected to a final output stage. The diagram is labeled with various logic symbols and equations, including "WINGCMQ PKGT QYC0382", "RTQGLWQY CP1", "CLWY C", "MQ PKGT QYC0372", "VT IPBM", "MQ PKGT QYC0372", "MY EKE", "FY WMQ PKGT QY", "RQD, 2N4470B", "RUPKGI EI", "Y QEQE 1 RQEQ 2", and "MY EKEKE PGMQ PKGT QY 2".

Diagram of a three-story building with a central vertical shaft. The building is symmetrical with rooms on both sides of the central shaft.

- Top Floor:**
 - Left side: YWMLCQ MQ PGT QY C0382, * MQ PGT, 0372, 0 WMLC0382
 - Right side: RTQGLWQY CP1, * Y QFQEK 1 RG0382
- Middle Floor:**
 - Left side: \ CLWY C*, MQ PGT, QYC*, 0.2
 - Right side: VT* IPBM, MQ PGT, QY \, 0382.2
- Bottom Floor:**
 - Left side: J \ FTCP V*, PCF, KOP1 0.2
 - Right side: MT* EKE*, FY WMQ PGT, QY \ RG, 0.27N3670.2
- Central Shaft:**
 - Bottom section: MONCPQ*, MQ PGT, QY G, MPYPO G0, 2

[illegible]

Diagram of a vertical shaft assembly with a shaft seal and a shaft seal gland. The shaft is labeled "VWMC P KGT QY C'0382'" and "P KGT QY C'0382'". The shaft seal is labeled "RTGLQ/MQY C P I" and "Y QF QEK I RGT0382". The shaft seal gland is labeled "V' IPBM" and "MQ821 QY I' 038212'". The shaft seal gland is also labeled "M' EKE'" and "FY WMQ P KGT QY I' 038212'". The shaft seal gland is also labeled "MNC P Q" and "MQ P KGT QY G'".

[illegible]